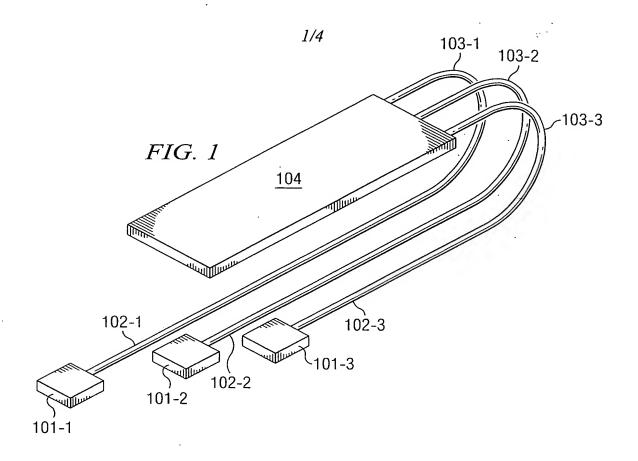
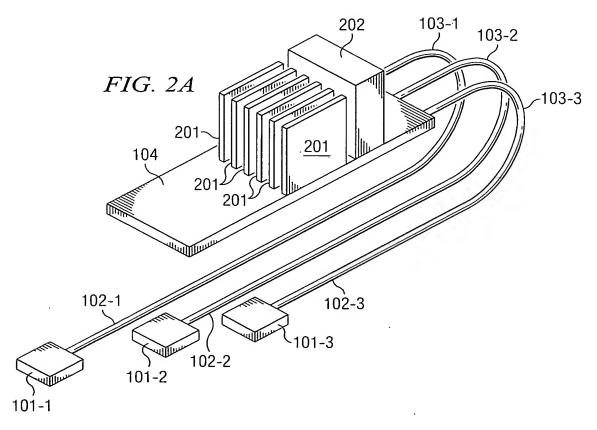
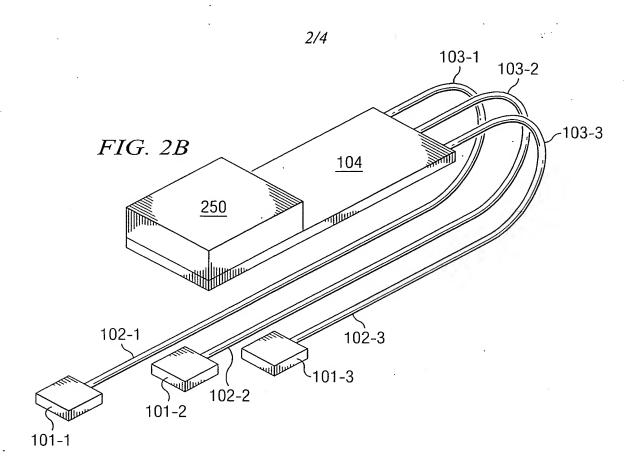
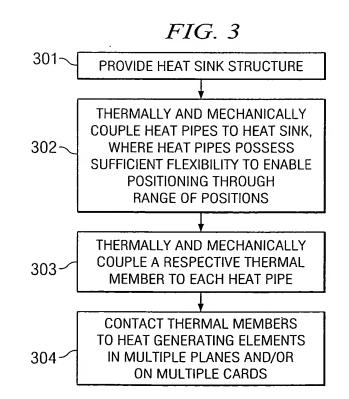
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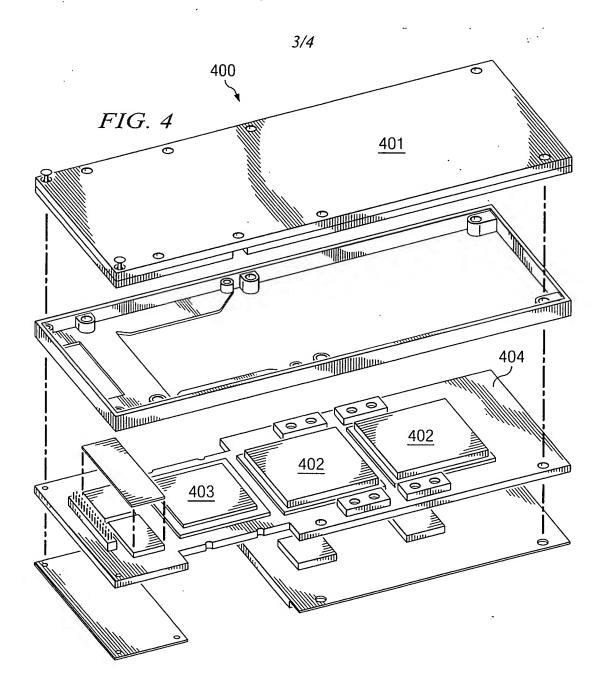


FIG. 5

501 500 503

POWER UNIT PROCESSOR CACHE MEMORY ASIC

502

